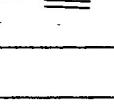
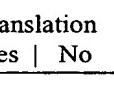
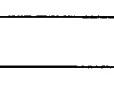


FORM PTO-1449 U.S. Department of Commerce Patent and Trademark Office				Attorney Docket Number: 5347-205			Serial No.: To Be Assigned <i>09/919,372</i>
LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary)				Applicants: Gutnik et al.			
				Filing Date: Currently Herewith		Group: Unknown	
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Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
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<i>pme</i>	4	Anceau, Francois; <i>A Synchronous Approach for Clocking VLSI Systems</i> ; IEEE Journal of Solid-State Circuits; Vol. SC-17, No. 1; February 1982; Pages 51 - 56					
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<i>pme</i>	6	Gutnik et al.; <i>Active GHz Clock Network Using Distributed PLLs</i> ; IEEE Journal of Solid-State Circuits; Vol. 35, No. 11; November 2000; Pages 1553 - 1560					
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